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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10085023	03/01/2002	257		2811	

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**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CON OF PCT/JP00/03468 05/30/2000

** FOREIGN APPLICATIONS /VERIFIED:

JAPAN 11-250780 09/03/1999

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	020137
Verified and Acknowledged Examiner's initials		
TITLE: Semiconductor memory and method of manufacture thereof		

U.S. DEPT. OF COMM. PA. FORM PTO-456, Rev. 11-94

		Assistant Examiner	Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
TERMINAL DISCLAIMER		Application Examiner		
PREPARED FOR ISSUE				
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